

HasteBoots: Proving FHE Bootstrapping in Seconds

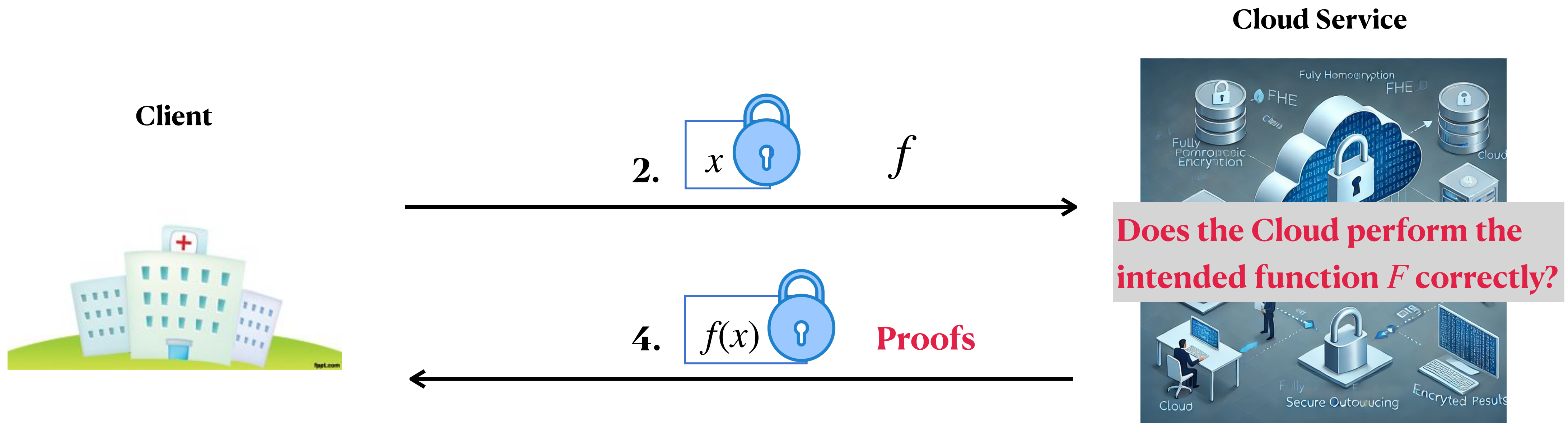
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Integrity Issues in FHE

FHE enables computation to be performed directly on encrypted data.

Application: Privacy-Preserving Cloud Computing



1. x (with lock icon) $= \text{Enc}_k(x)$

5. $f(x)$ $= \text{Dec}_k$ ($f(x)$ (with lock icon))

SNARKs check

3. $f(x)$ (with lock icon) $= F$ (x (with lock icon))

F is the FHE circuit w.r.t f

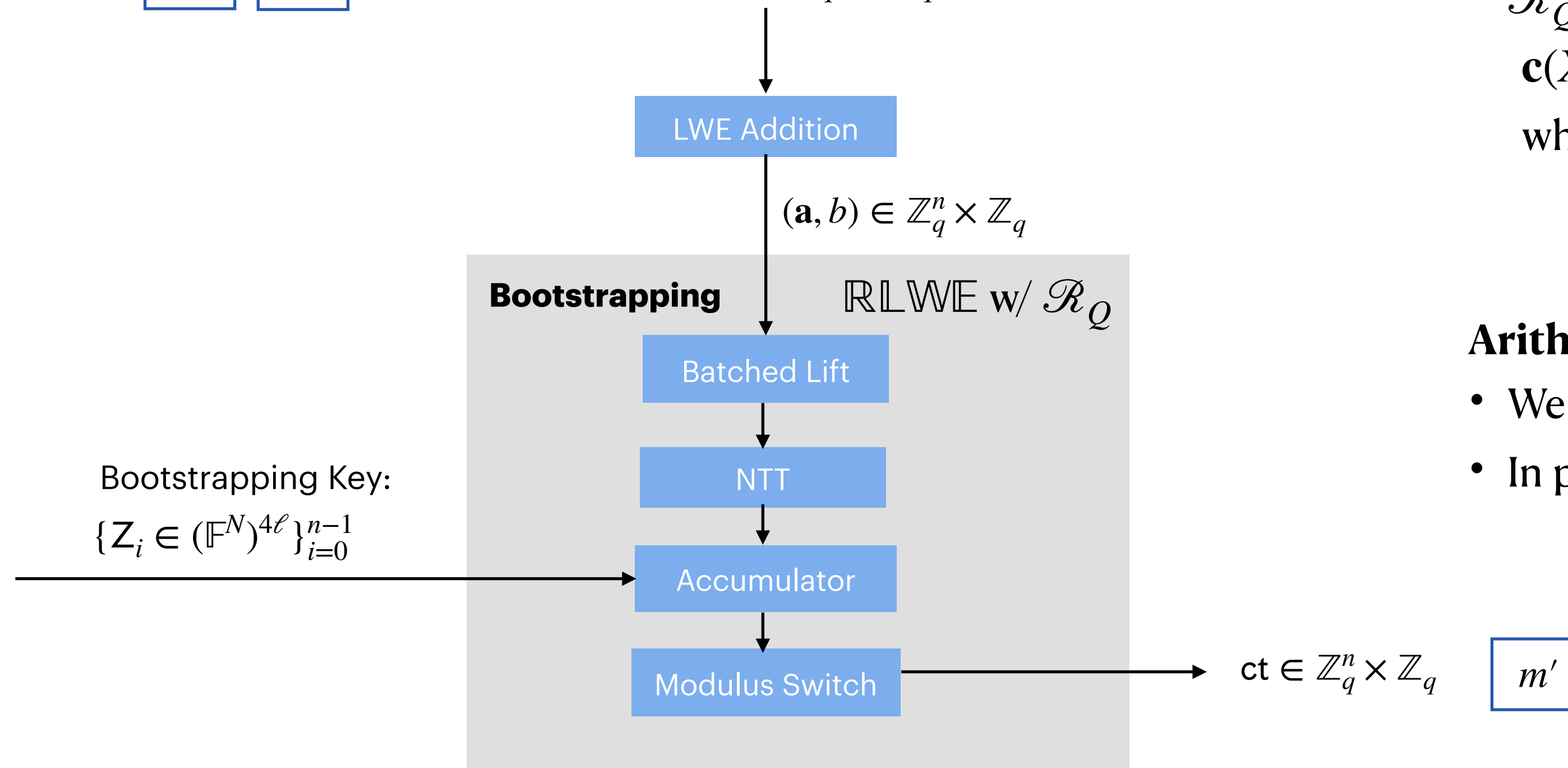
FHE NAND Operation

Full homomorphism requires the complex bootstrapping procedure

Given $m_0, m_1 \in \{0,1\}$, **NAND** computes $m' = \overline{m_0 m_1}$

In FHE: Given two FHE ciphertexts m_0 m_1 , **FHE NAND** computes m'

FHE NAND Workflow: m_0 m_1 \longrightarrow $ct_0, ct_1 \in \mathbb{Z}_q^n \times \mathbb{Z}_q$ (LWE ciphertexts)



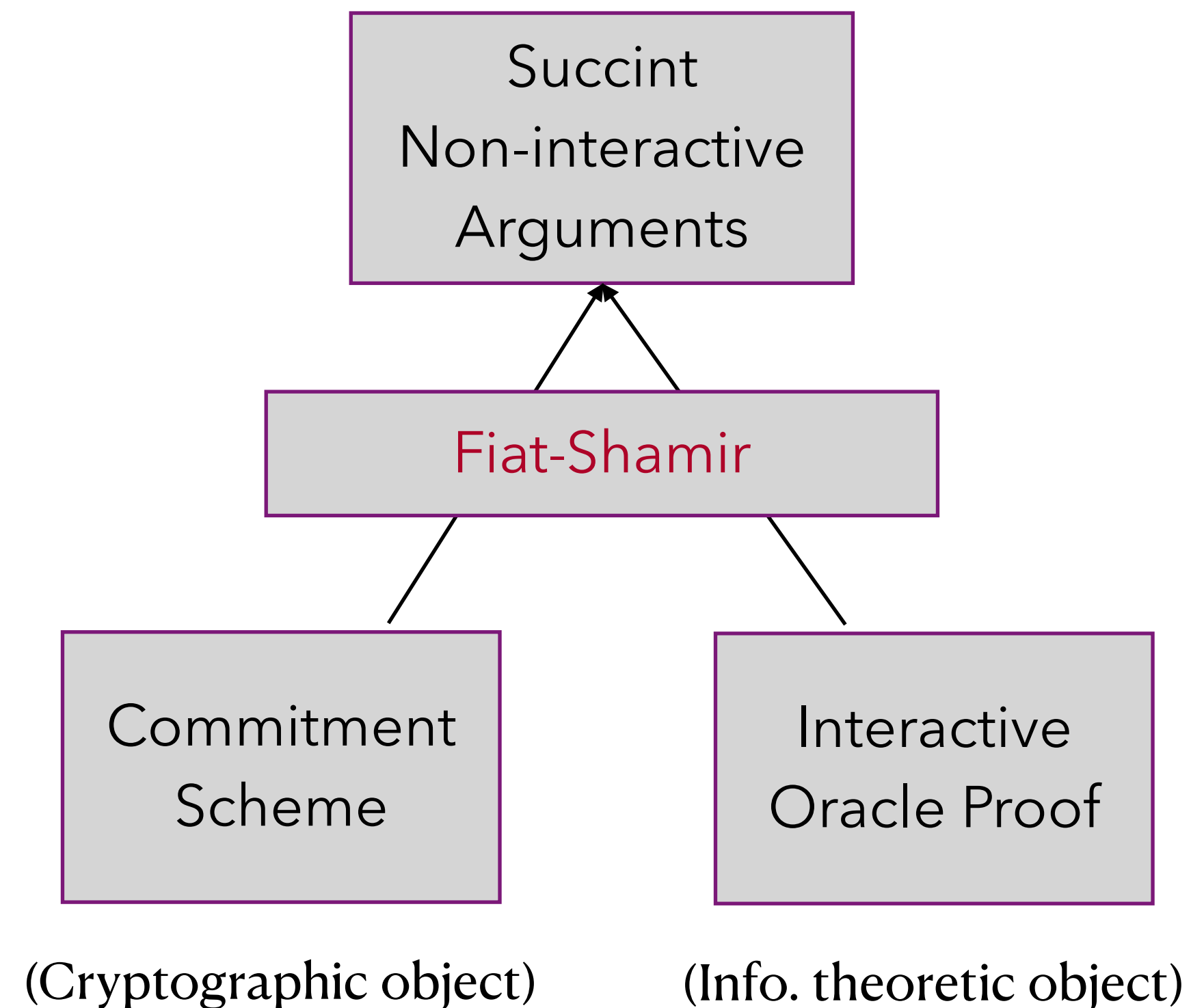
Notations:

- \mathbb{Z}_q : power-of-2 ring, e.g. $q = 1024$
- \mathbb{F}_Q : ~ 32 -bit prime field
- $\mathcal{R}_Q = \mathbb{F}_Q[X]/(X^N + 1)$: polynomial ring
 $\mathbf{c}(X) = c_0 + \dots + c_{N-1}X^{N-1} \in \mathcal{R}_Q$
 where $c_i \in \mathbb{F}_Q$ (rep. with $\mathbf{c} \in \mathbb{F}^N$)

Arithmetic in the proof system:

- We use \mathbb{F}_Q in line with FHE
- In practice, we use $(\mathbb{F}_Q)^D$ for soundness

Paradigm for Building Succinct Arguments

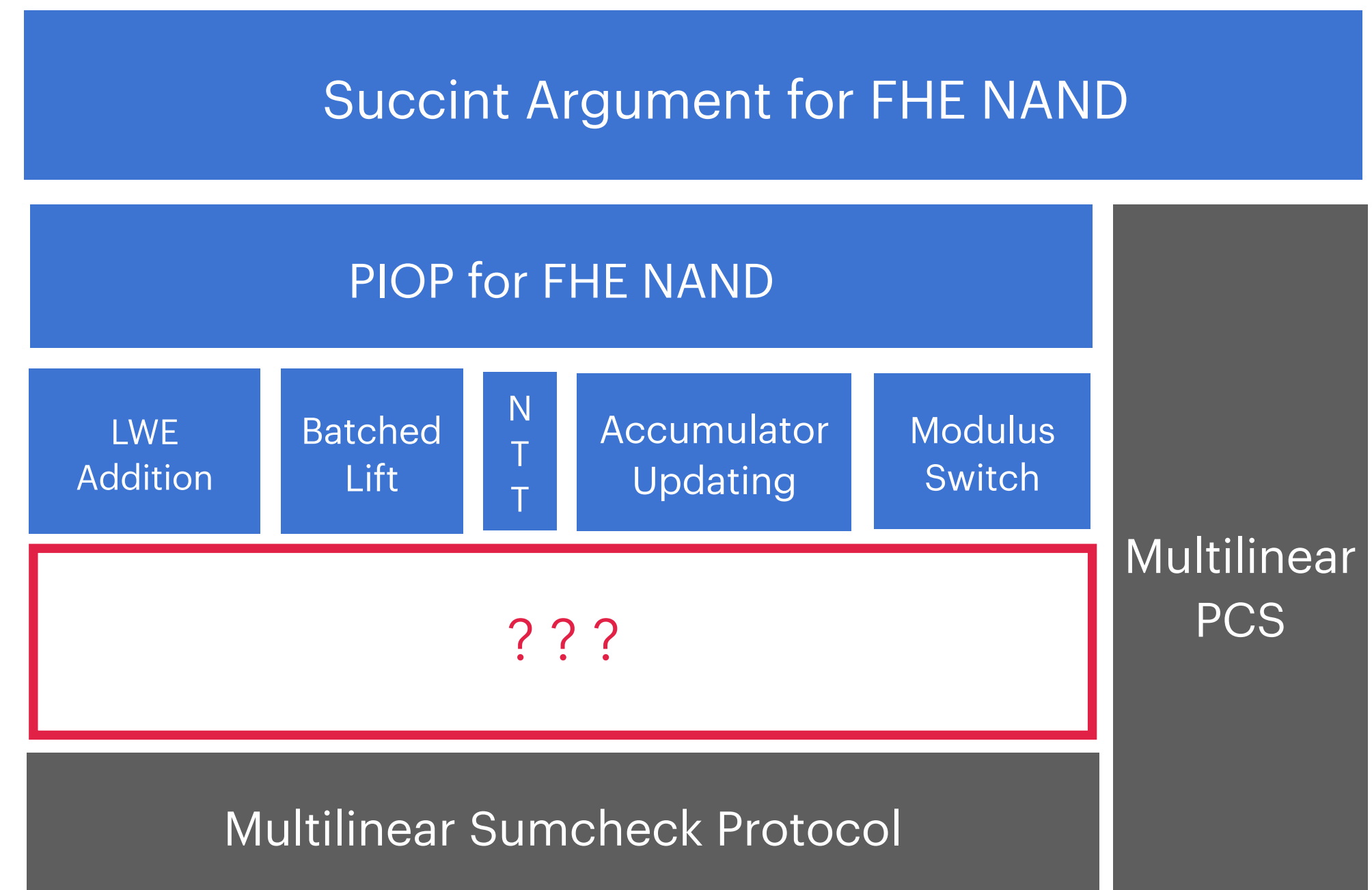


Our choice

Multilinear PCS
Brakedown

Multilinear Polynomial IOP
Sumcheck Protocol

Our Protocol Design



Step 1: LWE Addition

FHE NAND Workflow: m_0 m_1 \longrightarrow $ct_0, ct_1 \in \mathbb{Z}_q^n \times \mathbb{Z}_q$ (LWE ciphertexts)

Notations:

- \mathbb{Z}_q : power-of-2 ring, e.g. $q = 1024$
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LWE Addition

$(\mathbf{a}, b) \in \mathbb{Z}_q^n \times \mathbb{Z}_q$

LWE Addition:

Given $(\mathbf{a}_1, b_1), (\mathbf{a}_2, b_2) \in \mathbb{Z}_q^n \times \mathbb{Z}_q$, computes $(\mathbf{a}_1 + \mathbf{a}_2, b_1 + b_2) \pmod q$.

Core Relation: Given $a, b, c \in \mathbb{Z}_q$, check that $a + b = c \pmod q$

Range Check



$$\exists w \in \{0,1\} \text{ such that } a + b = w \cdot q + c \pmod Q$$



$$w \cdot (1 - w) = 0$$

Hadamard

Range Check

- check $\mathbf{c} \in \mathbb{Z}_q^N$ (q is small)
- check $\mathbf{c} \in \mathbb{F}_K^N$ (K is as large as Q)

Hadamard

(Sumcheck)

- check $\mathbf{a} \circ \mathbf{b} = \mathbf{c}$ ($c_i = a_i \cdot b_i$)
- check $\sum_{i=1}^M \mathbf{a}_i \circ \mathbf{b}_i = \mathbf{c}$

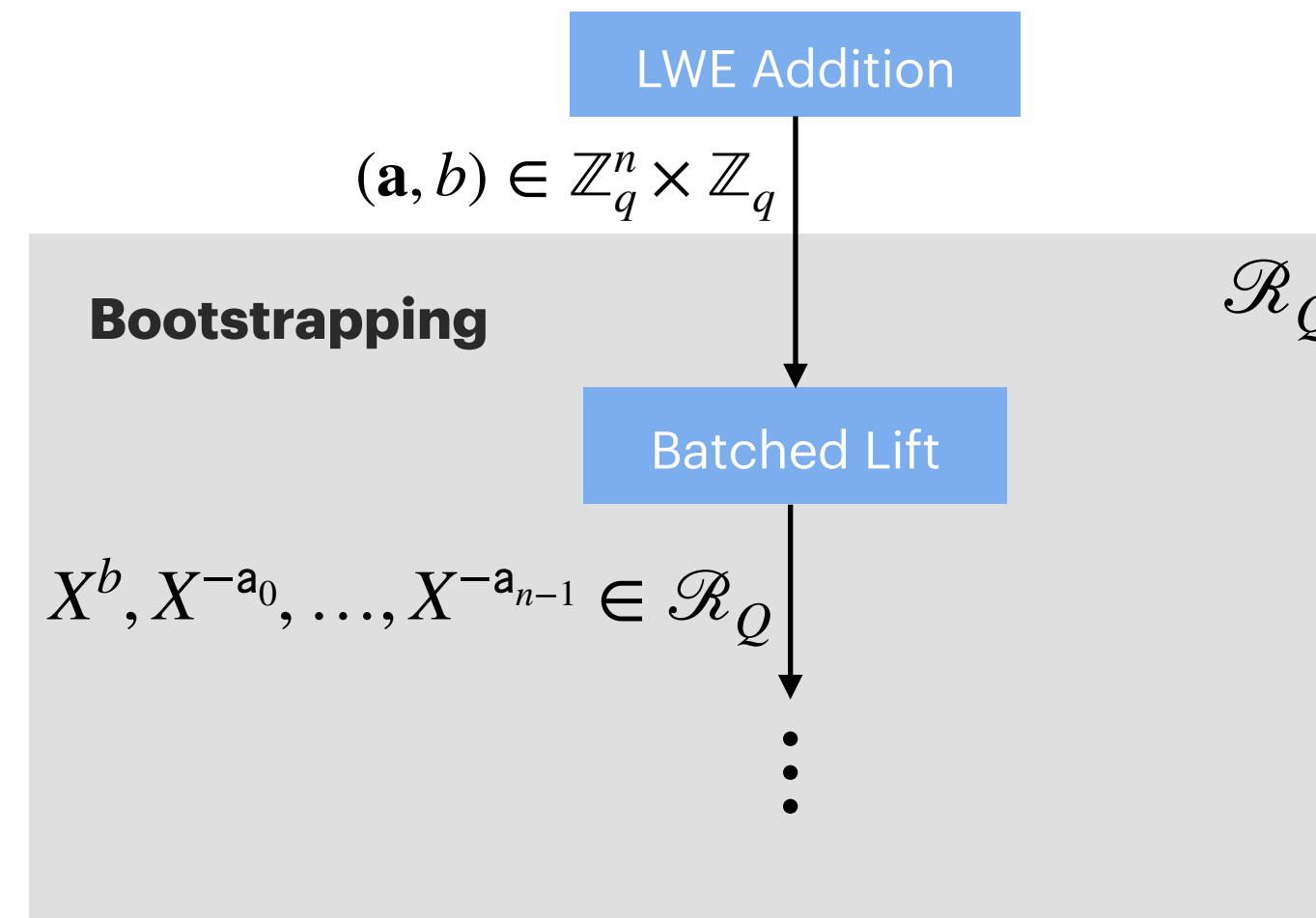
Step 2: Batched Lift

FHE NAND Workflow:

Q: Why Perform Lift?

It enables bootstrapping for **homomorphic decryption**.

For an LWE ciphertext (\mathbf{a}, b) under the secret key \mathbf{s} , the decryption circuit is $\left\lfloor \frac{b - \langle \mathbf{a}, \mathbf{s} \rangle}{q/4} \right\rfloor$



$\mathcal{R}_Q = \mathbb{F}_Q / (X^N + 1)$ contains $\{X, X^2, \dots, X^{2N}\}$

assm. $q = 2N$

Batched Lift: Given $(\mathbf{a}, b) \in \mathbb{Z}_q^n \times \mathbb{Z}_q$, computes $n + 1$ polynomials denoted by $X^b, X^{-a_0}, \dots, X^{-a_{n-1}} \in \mathbb{F}^N$.

$$X^{b - \sum_{i=0}^{n-1} a_i \cdot s_i}$$

Core Relation: Given $s \in \mathbb{Z}_q$ and $\mathbf{c} \in \mathbb{F}^N$, check that $X^s = \mathbf{c}(X) \pmod{X^N + 1}$

assm. $q = 2N$

$$\exists k \in \{0, 1\} \text{ s.t. } \underset{[2N]}{s} = \underset{[N]}{k} \cdot N + r \quad \text{and} \quad \mathbf{c}(X) = \begin{cases} X^r & \text{if } k = 0 \\ -X^r & \text{if } k = 1 \end{cases}$$

Sparse!

$\mathbf{c} \in \mathbb{F}^N$ contains only one non-zero entry of value $1 - 2k$, located at r

Q: What computation is performed on $\mathbf{c}(X)$?

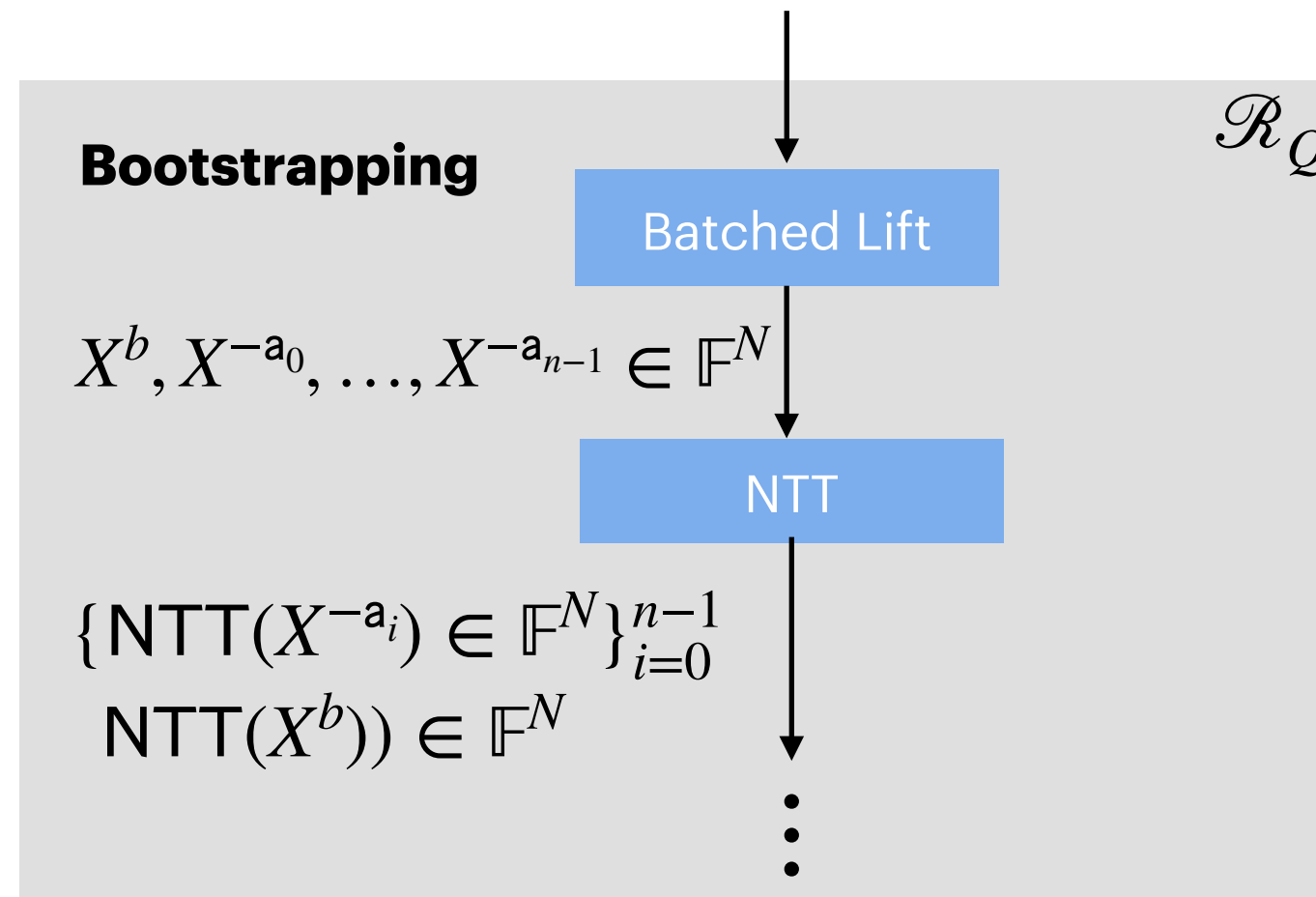
Step 2: Batched Lift + NTT

assm. $q = 2N$

FHE NAND Workflow:

Q: Why Perform NTT?

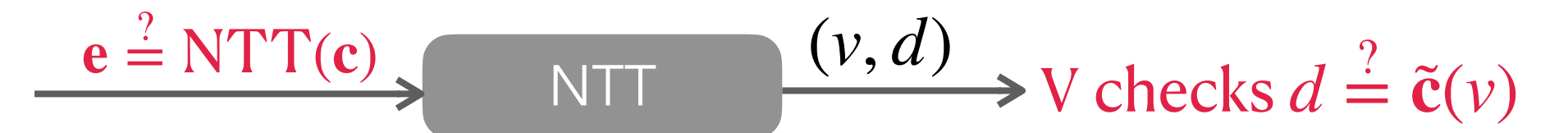
It enables quasi-linear polynomial multiplication.



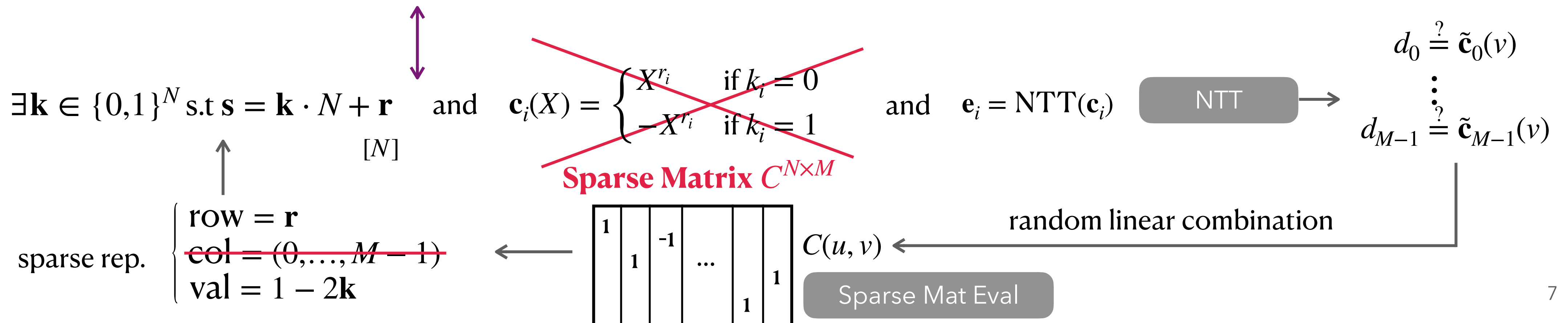
Batched Lift: Given $(\mathbf{a}, b) \in \mathbb{Z}_q^n \times \mathbb{Z}_q$, computes $n + 1$ polynomials denoted by $X^b, X^{-a_0}, \dots, X^{-a_{n-1}} \in \mathbb{F}^N$.

+

NTT: Given a polynomial $\mathbf{c}(X) \in \mathcal{R}_Q$ with **coefficient vector** $\mathbf{c} \in \mathbb{F}^N$, computes the **evaluation vector** $\mathbf{e} \in \mathbb{F}^N$, where e_i corresponds to the evaluation at point ω^{2i-1} .



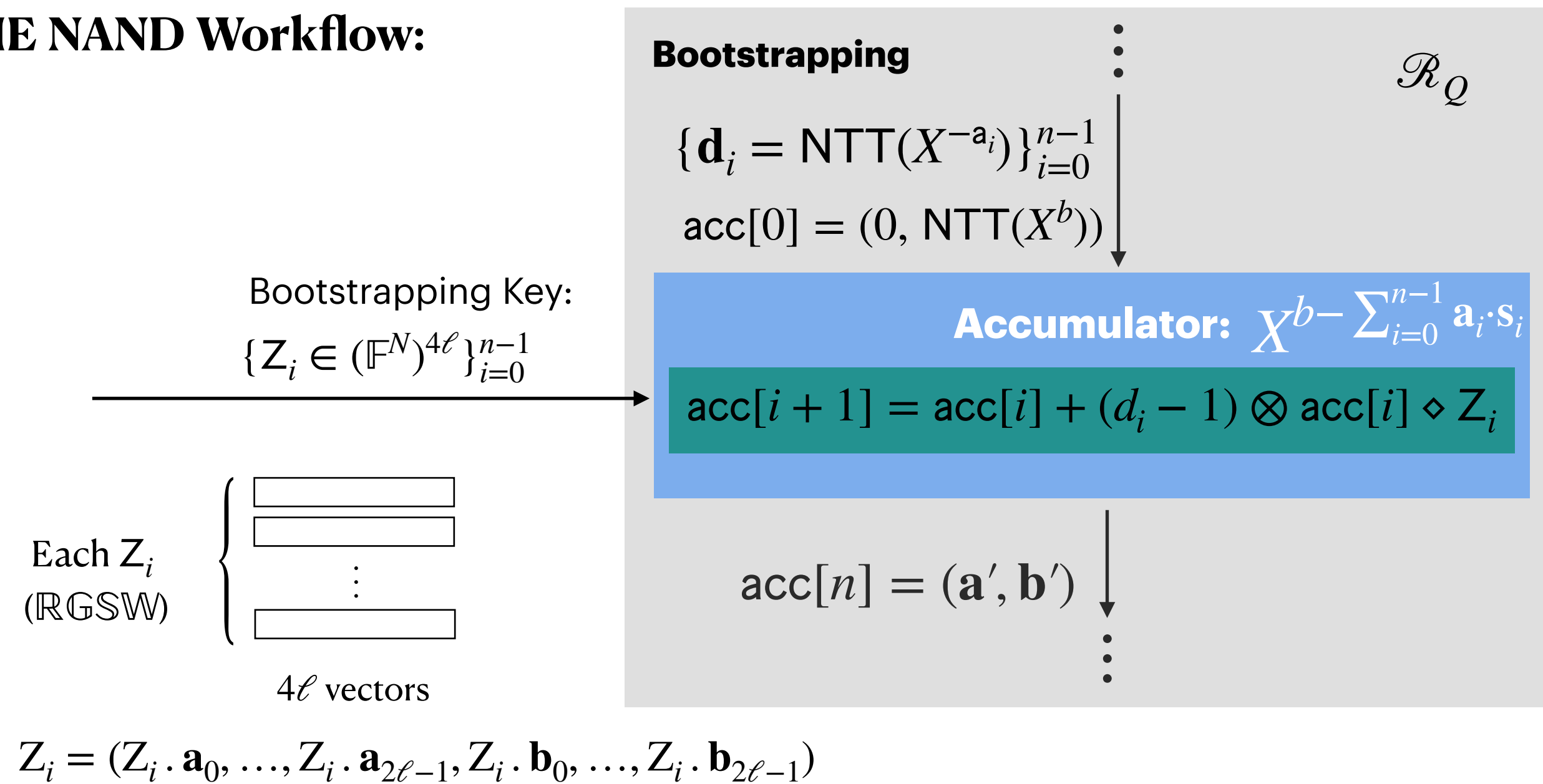
Batched Relation: Given $s \in \mathbb{Z}_q^M$ and $\mathbf{e}_0, \dots, \mathbf{e}_M \in \mathbb{F}^N$, check that $\mathbf{e}_i = \text{NTT}(X^{s_i} \text{ mod } X^N + 1)$ for $i \in [M]$



Step 3: Accumulator Updating

$$\mathbf{x} = \sum_{i=0}^{\ell-1} B^i \cdot \mathbf{a}_i$$

FHE NAND Workflow:



Op \diamond : $(\mathbb{F}^N, \mathbb{F}^N) \diamond (\mathbb{F}^N)^{4\ell} \rightarrow (\mathbb{F}^N, \mathbb{F}^N)$

$(\mathbf{x}, \mathbf{y}) \diamond Z_i = (\mathbf{a}', \mathbf{b}')$

1. Decompose \mathbf{x} and \mathbf{y} into 2ℓ "bits"

$\text{bits}[2\ell] = (\mathbf{a}_0, \dots, \mathbf{a}_{\ell-1}, \mathbf{b}_0, \dots, \mathbf{b}_{\ell-1})$

2. Perform **NTT** on each "bit" to obtain "Nbits"

$\text{Nbits}[i] = \text{NTT}(\text{bits}[i])$ for $i = 0..2\ell - 1$

3. Compute

$$\left(\sum_{i=0}^{2\ell-1} \text{Nbits}[i] \circ Z_i \cdot \mathbf{a}_i, \sum_{i=0}^{2\ell-1} \text{Nbits}[i] \circ Z_i \cdot \mathbf{b}_i \right)$$

Gadget Dec

NTT

Hadamard

INTT: inverse of NTT

Op \otimes : $\mathbb{F}^N \otimes (\mathbb{F}^N, \mathbb{F}^N) \rightarrow (\mathbb{F}^N, \mathbb{F}^N)$

$\mathbf{d} \otimes (\mathbf{a}, \mathbf{b}) = (\text{INTT}(\mathbf{d} \circ \mathbf{a}), \text{INTT}(\mathbf{d} \circ \mathbf{b}))$

Hadamard

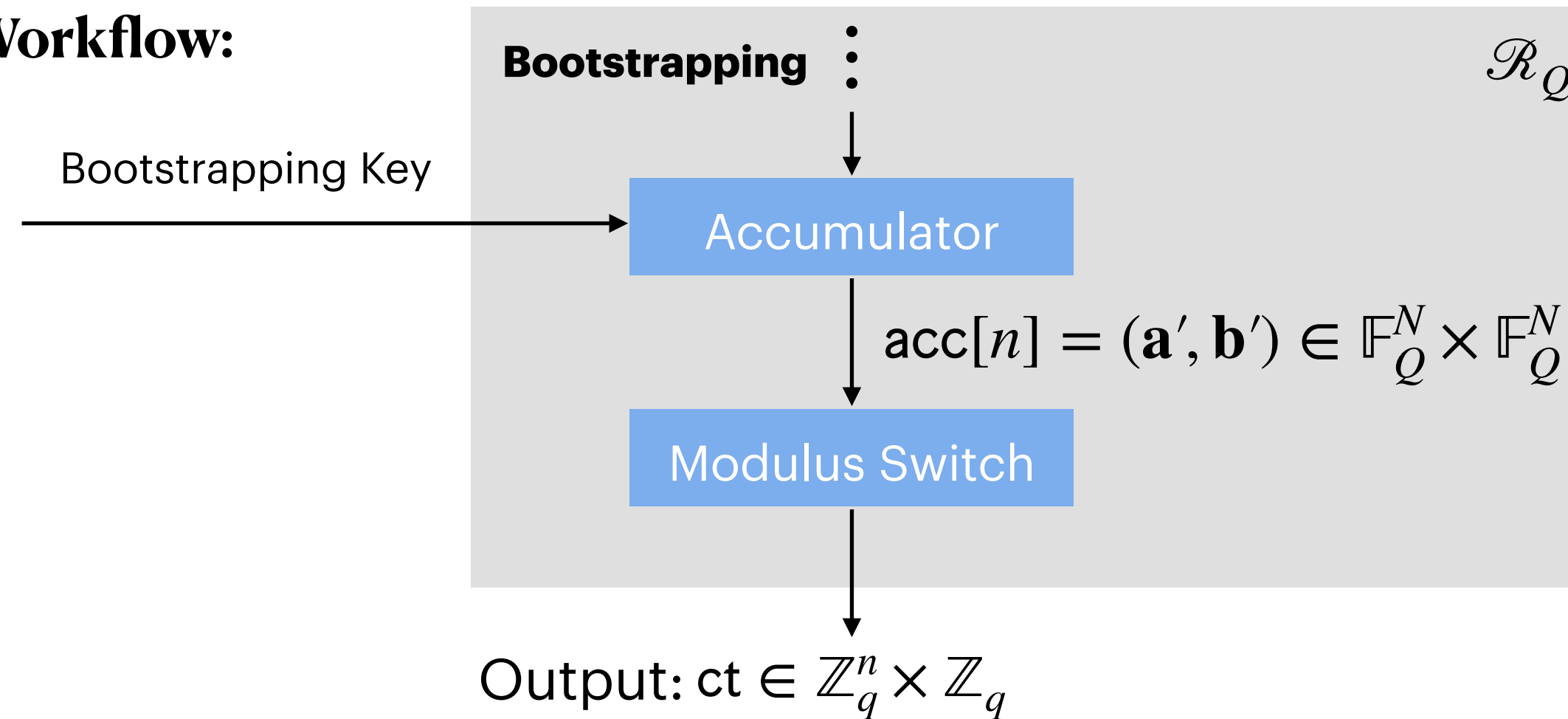
NTT

Core Relation:

- 4 (sums) of Hadamard products
- 2 Gadget Decomposition
- $2\ell + 2$ NTT/INTT

Step 4: Modulus Switch

FHE NAND Workflow:



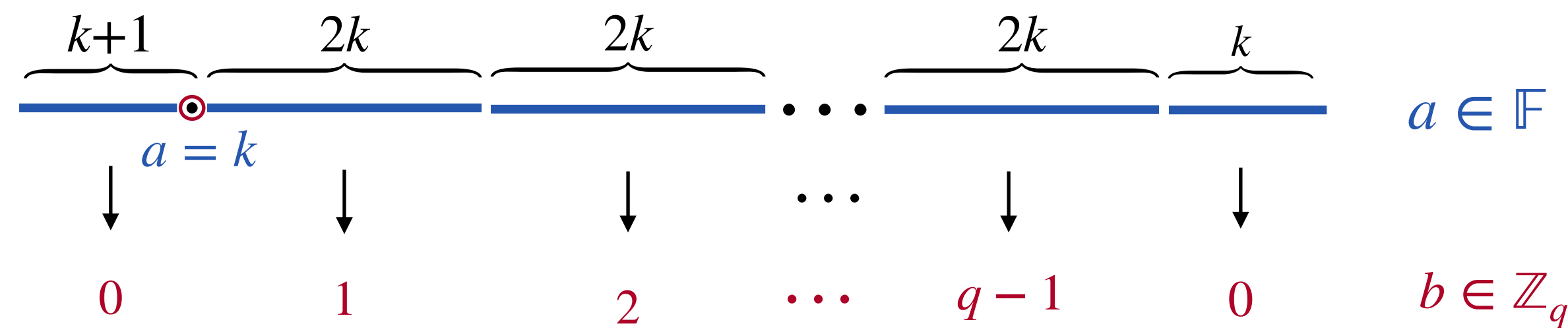
Modulus Switch from \mathbb{F}_Q to \mathbb{Z}_q :

Given $a \in \mathbb{F}_Q$, compute

$$b = \left\lfloor \frac{a \cdot q}{Q} \right\rfloor \pmod{q} \in \mathbb{Z}_q$$

Note: $\left\lfloor \frac{a \cdot q}{Q} \right\rfloor$ could be q .

Assm. $2q \mid Q - 1$, define $k = \frac{Q - 1}{2q}$:



$$a \in [0, k] \cup [Q - k, Q) \mapsto b = 0$$

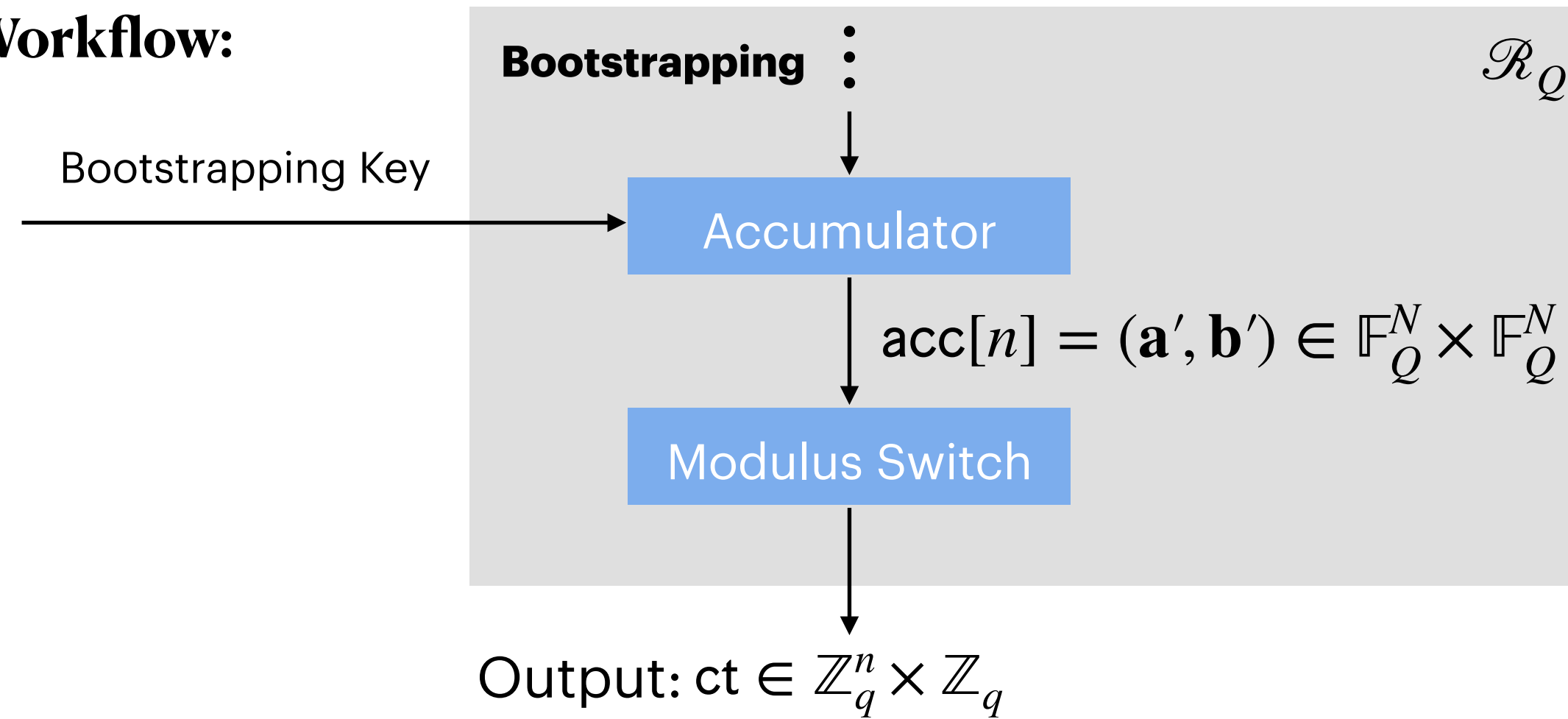
$$= k \cup [(2q - 1) \cdot k, (2q + 1) \cdot k]$$

$$a \in [(2b - 1) \cdot k + 1, (2b + 1) \cdot k] \mapsto b$$

for $b \in \{1, \dots, q - 1\}$

Step 4: Modulus Switch

FHE NAND Workflow:

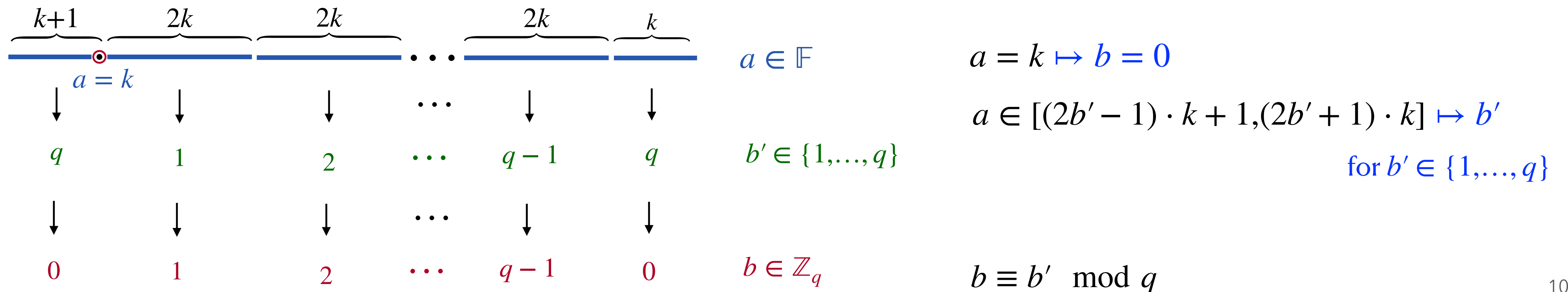


Modulus Switch from \mathbb{F}_Q to \mathbb{Z}_q :
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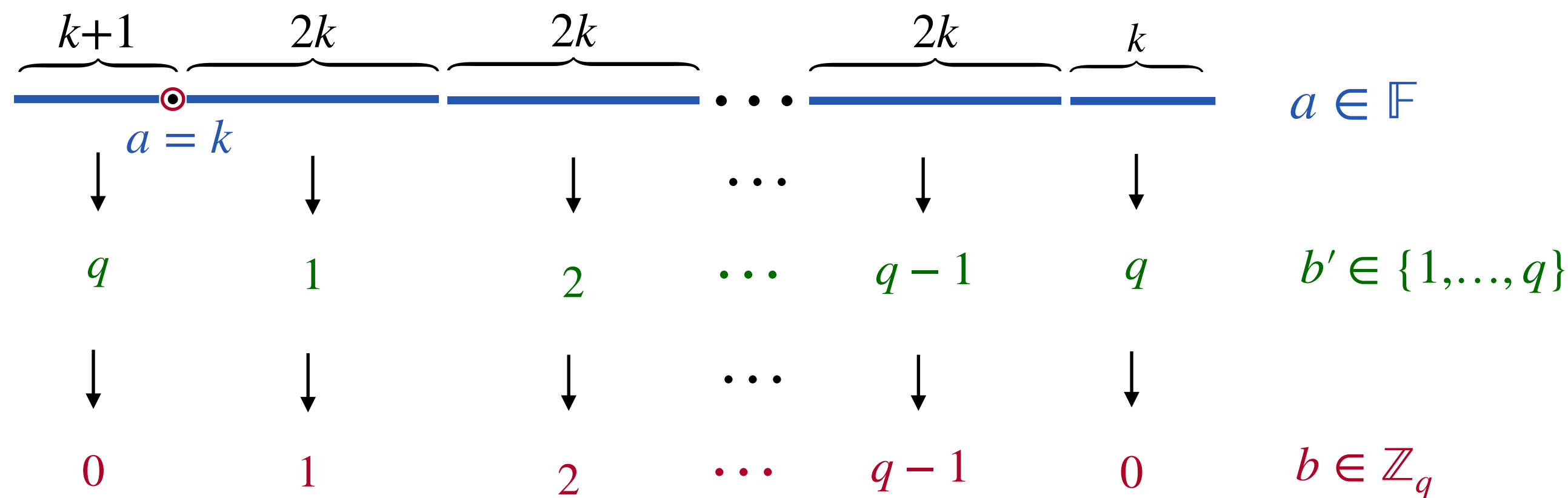
$$b = \left\lfloor \frac{a \cdot q}{Q} \right\rfloor \pmod q \in \mathbb{Z}_q$$

Note: $\left\lfloor \frac{a \cdot q}{Q} \right\rfloor$ could be q .

Assm. $2q \mid Q - 1$, define $k = \frac{Q - 1}{2q}$:



Step 4: Modulus Switch



Assm. $2q \mid Q - 1$, define $k = \frac{Q-1}{2q}$. (k is large $\sim Q$)

Modulus Switch from \mathbb{F}_Q to \mathbb{Z}_q :
 Given $a \in \mathbb{F}_Q$, compute

$$b = \left\lfloor \frac{a \cdot q}{Q} \right\rfloor \pmod q \in \mathbb{Z}_q$$

Core Relation: Given $a \in \mathbb{F}_Q$ and $b \in \mathbb{Z}_q$, check that $b = \left\lfloor \frac{a \cdot q}{Q} \right\rfloor \pmod q \in \mathbb{Z}_q$

Dichotomy:

$\exists w \in \{0,1\}$

$$\begin{cases} w=1 & \left\{ \begin{array}{l} a = k \quad \&\& \quad b = 0 \\ w=0 & \left\{ \begin{array}{l} a \in [(2b'-1) \cdot k + 1, (2b'+1) \cdot k] \end{array} \right. \end{array} \right.$$

and $b' \in \{1, \dots, q\}$ and $b \equiv b' \pmod q$

Hadamard

Range Check

$$\begin{cases} w=1 & \left\{ \begin{array}{l} p = \lambda_1 \cdot (a - k) + \lambda_2 \cdot b = 0 \\ w=0 & \left\{ \begin{array}{l} c = a - (2b'-1) \cdot k - 1 \quad \text{and} \quad c \in [0, 2k) \end{array} \right. \end{array} \right.$$

Range Check

$\exists k \in \{0,1\} \quad b' = b + k \cdot q$

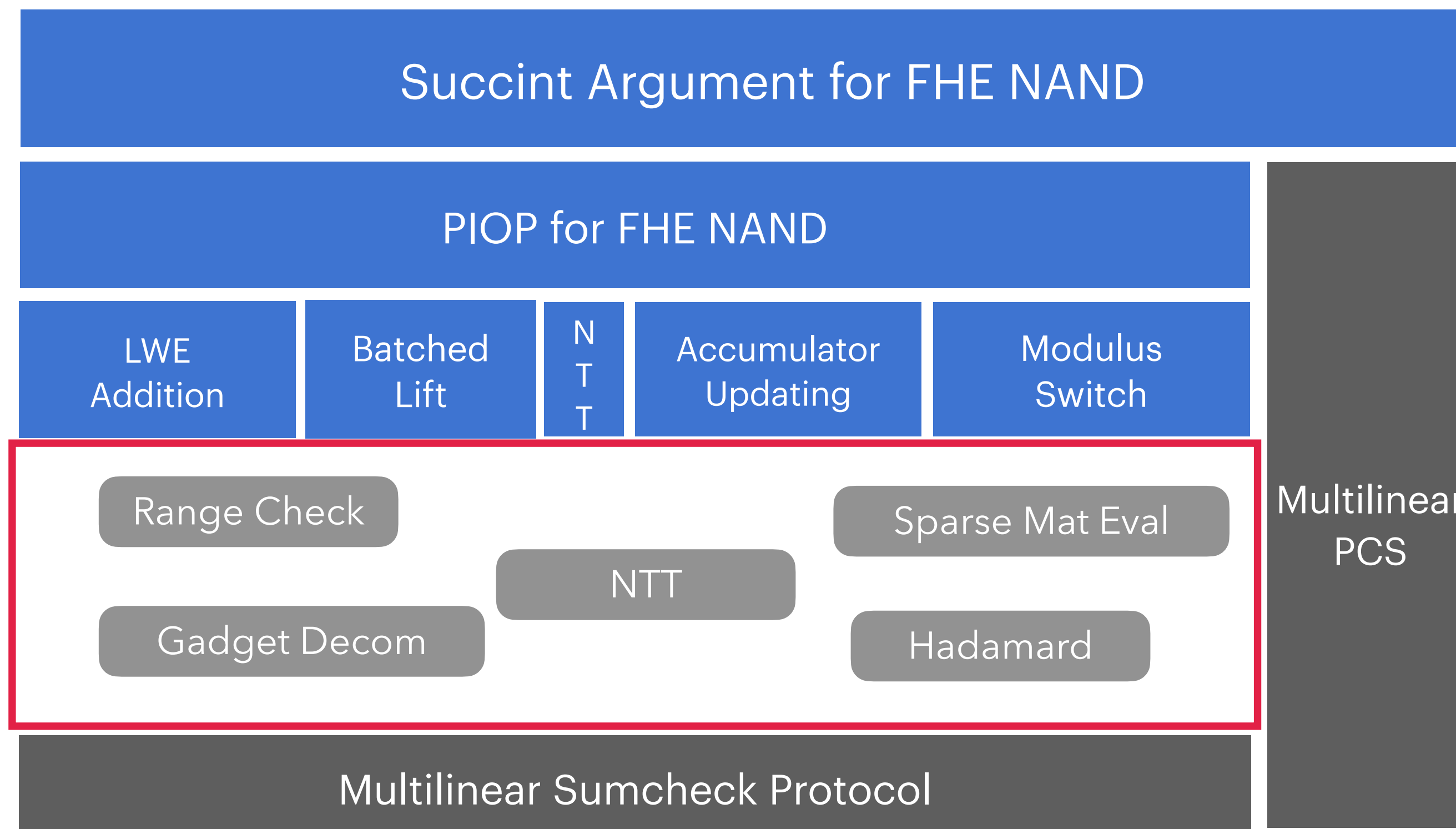
Hadamard

Hadamard

$$w \cdot p + (1 - w) \cdot (a - (2b' - 1) \cdot k - 1 - c) = 0$$

Back to Our Protocol Design

5 Building Blocks

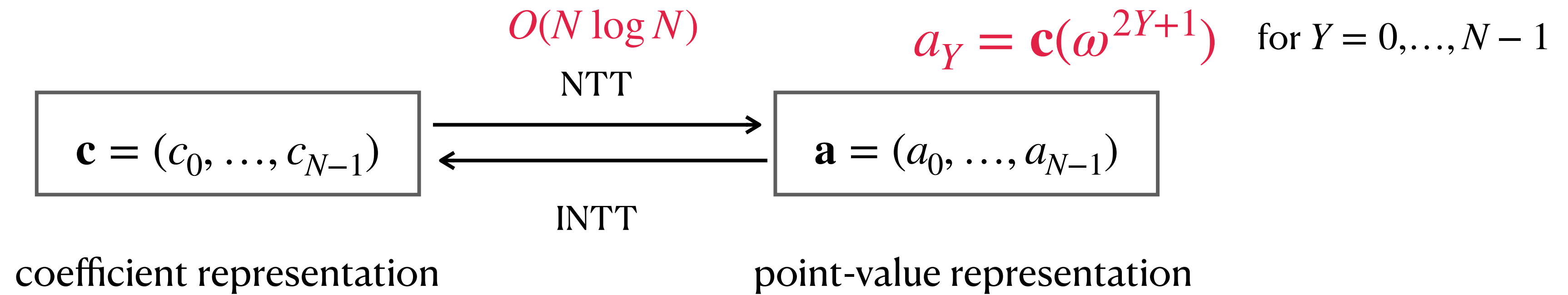


Instantiate Building Blocks

NTT

$$\mathbf{c}(X) = c_0 + c_1X + \dots + c_{N-1}X^{N-1} \in \mathbb{F}_Q[X]/(X^N + 1)$$

ω : $2N$ -th roots of unity s.t. $\omega^{2N} = 1$



$$a_Y = \mathbf{c}(\omega^{2Y+1}) \quad \text{for } Y = 0, \dots, N-1$$

fast NTT: use bit-reversed order

normal order $X = \sum_{i=0}^{\ell-1} 2^i \cdot x_i$

bit-reversed order $X^R = \sum_{i=0}^{\ell-1} 2^{\log N - 1 - i} \cdot x_i$

$$\mathbf{a} = (a_{000}, a_{001}, a_{010}, a_{011}, a_{100}, a_{101}, a_{110}, a_{111})$$

$$\mathbf{a}^R = (a_{000}, a_{100}, a_{010}, a_{110}, a_{001}, a_{101}, a_{011}, a_{111})$$

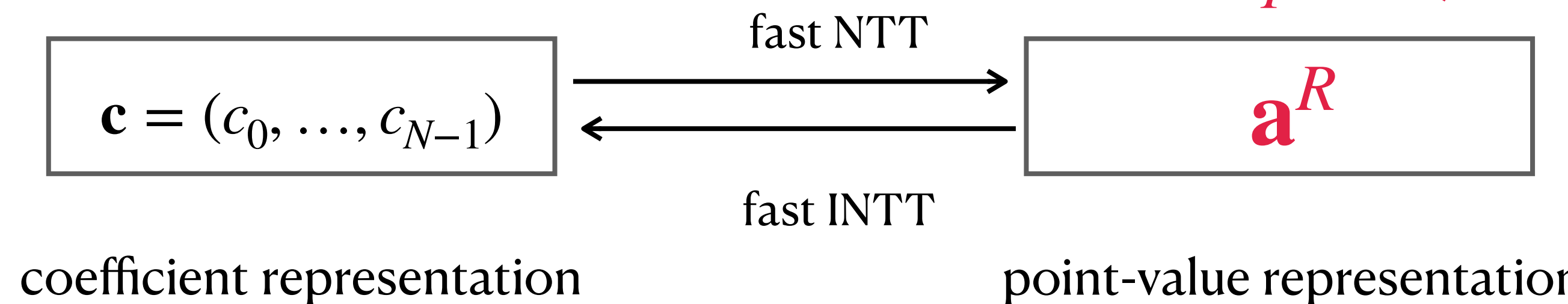
Instantiate Building Blocks

NTT

$$\mathbf{c}(X) = c_0 + c_1X + \dots + c_{N-1}X^{N-1} \in \mathbb{F}_Q[X]/(X^N + 1)$$

$$\omega: 2N\text{-th roots of unity s.t. } \omega^{2N} = 1$$

$$a'_Y = \mathbf{c}(\omega^{2Y^R+1}) \text{ for } Y = 0, \dots, N-1$$



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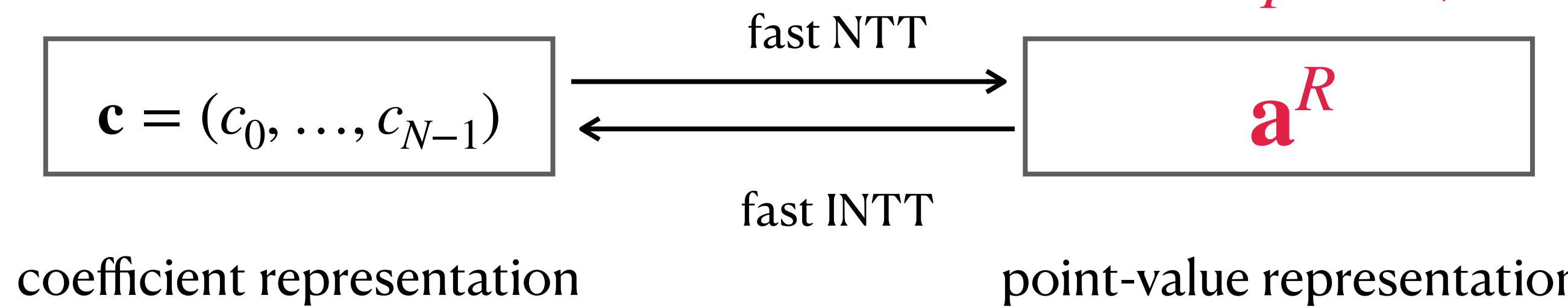
Instantiate Building Blocks

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$$a'_Y = \mathbf{c}(\omega^{2Y^R+1}) \text{ for } Y = 0, \dots, N-1$$



fast NTT: use bit-reversed order

$$\mathbf{a}^R = F^R \cdot \mathbf{c} \quad \text{where } F^R \text{ is } N \times N \text{ matrix defined as } F^R(Y, X) = \omega^{(2Y^R+1) \cdot X}$$

$$\tilde{\mathbf{a}}^R(y) = \sum_{x \in \{0,1\}^{\log N}} \tilde{F}^R(y, x) \cdot \tilde{\mathbf{c}}(x) \quad \text{for } y \in \{0,1\}^{\log N}$$

Schwartz-Zippel Lemma

$$\tilde{\mathbf{a}}^R(u) = \sum_{x \in \{0,1\}^{\log N}} \tilde{F}^R(u, x) \cdot \tilde{\mathbf{c}}(x) \quad \text{where } u \in \mathbb{F}$$

Sumcheck

Instantiate Building Blocks

$$\tilde{\mathbf{a}}^R(u) = \sum_{x \in \{0,1\}^{\log N}} \tilde{F}^R(u, x) \cdot \tilde{\mathbf{c}}(x) \quad \text{where } u \in \mathbb{F}$$

Sumcheck

Idea from [LXZ 21]:

- $\omega^{2^{\log N - i}} = \omega^{\frac{2N}{2^{i+1}}}$ is the 2^{i+1} -th roots of unity
- $2Y^R = \sum_{i=0}^{\log N - 1} 2^{\log N - i} \cdot y_i$
- $\omega^{2Y^R} = \prod_{i=0}^{\log N - 1} \left(\omega^{2^{\log N - i}} \right)^{y_i}$
- Decompose the exponents of ω
- Divide the computation into $\log N$ rounds via a dynamic algorithm

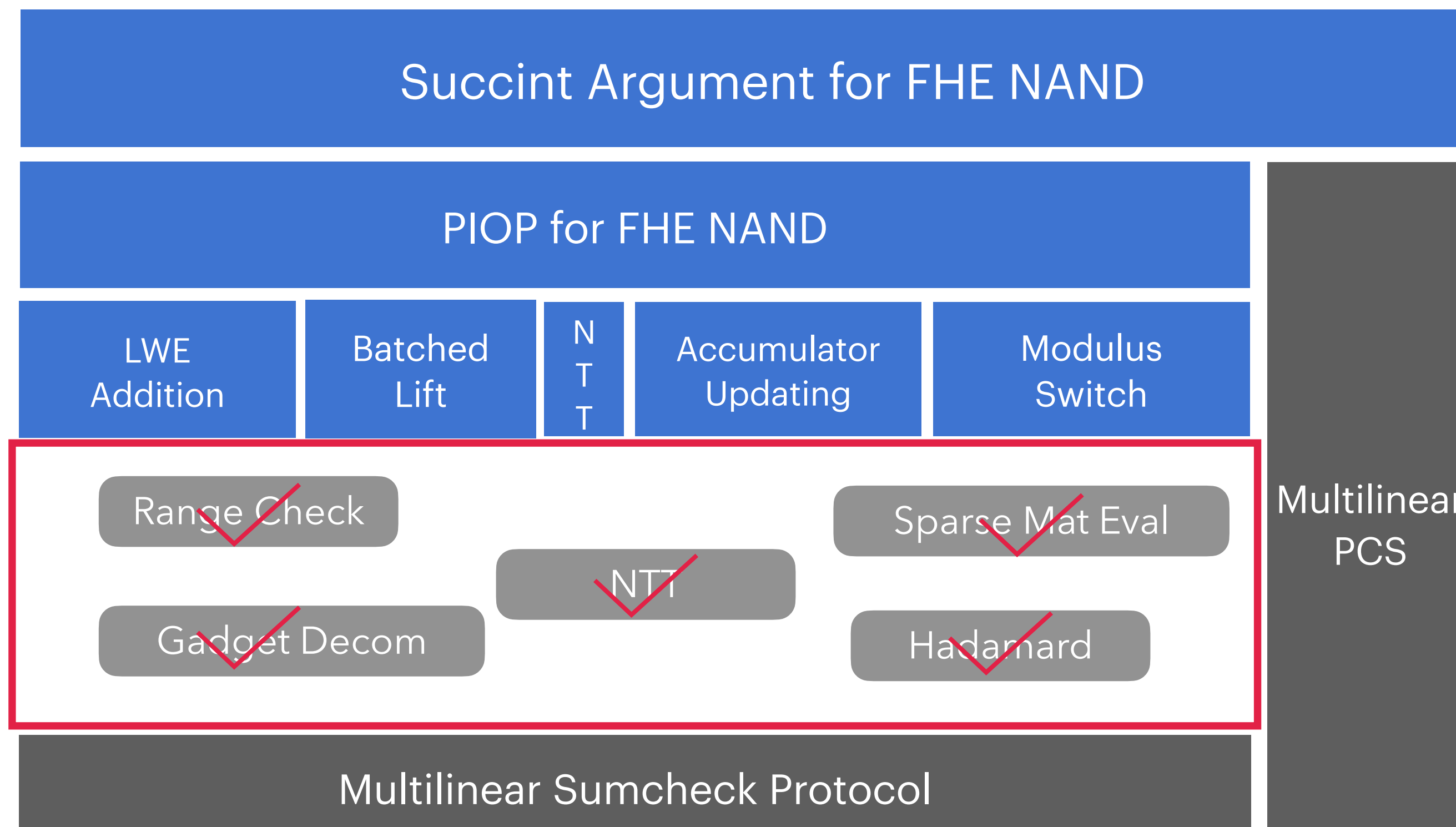
Compute $\tilde{F}^R(u, x)$ in $O(N)$!

$$\begin{aligned} \tilde{F}^R(u, x) &= \sum_{y \in \{0,1\}^{\log N}} \tilde{e}q(u, y) \tilde{F}^R(y, x) \\ &= \sum_{y \in \{0,1\}^{\log N}} \tilde{e}q(u, y) \omega^{(2Y^R + 1) \cdot \mathcal{X}} \\ &= \omega^{\mathcal{X}} \cdot \sum_{y \in \{0,1\}^{\log N}} \tilde{e}q(u, y) \omega^{\mathcal{X} \cdot 2Y^R} \\ &= \prod_{i=0}^{\log N - 1} (1 - u_i + u_i \cdot \omega_{2^{i+1}}^{\mathcal{X}}) \cdot \omega^{2^i \cdot x_i} \end{aligned}$$

$$* \omega_{2^{i+1}} = \omega^{2^{\log N - i}} = \omega^{\frac{2N}{2^{i+1}}}$$

Back to Our Protocol Design

5 Building Blocks



Performance

Proving time for a single bootstrapping operation

Proving Time	zkVM		Plonky2	Ours
	R1SC0	SP1	Zama	
M3 Pro (8 cores)	-		40 min	7 s
C61.meta (128 cores)	-		21 min	5 s
Hpc7a.96xlarge (192 cores)	4600 min	1500 min	18 min	4 s
M4 Pro	-			3 s

Thank you for your attention!

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